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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/960,412	09/24/2001	Noboru Kageyama	32213M015	5634	
75	90 02/07/2003				
Smith, Gambrell & Russell, LLP Beveridge, DeGrandi, Weilacher & Young Intellectual Property Group			EXAMINER		
			BUDD, MARK OSBORNE		
1850 M Street, N.W. Suite 800 Washington, DC 20036			ART UNIT	ART UNIT PAPER NUMBER	
washington, D	20030		2834		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Commissions	960417					
Office Action Summary	Examiner M. Bud	Group Art Unit 2834				
	on the cover sheet be	neath the correspondence a	ddress-			
Period for Reply	7					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO OF THIS COMMUNICATION.	EXPIRE	MONTH(S) FROM THE MA	AILING DATE			
<ul> <li>Extensions of time may be available under the provisions of 37 CFR 1. from the mailing date of this communication.</li> <li>If the period for reply specified above is less than thirty (30) days, a reply lift NO period for reply is specified above, such period shall, by default,</li> <li>Failure to reply within the set or extended period for reply will, by statuenth and the period by the Office later than three months after the mailing term adjustment. See 37 CFR 1.704(b).</li> </ul>	ly within the statutory mini expire SIX (6) MONTHS fro te, cause the application to	mum of thirty (30) days will be cons m the mailing date of this communi b become ABANDONED (35 U.S.C.	idered timely. cation. § 133).			
Status	•					
Responsive to communication(s) filed on 1-3-0	<u> </u>		·			
☐ This action is FINAL.						
☐ Since this application is in condition for allowance except faccordance with the practice under Ex parte Quayle, 1935			closed in			
Disposition of Claims	ת ס					
Claim(s) 1, 5-13 and 17-	3 5	is/are pending in the app	olication.			
Of the above claim(s)		is/are withdrawn from co	onsideration.			
Claim(s)	is/are allowed.					
Claim(s) 1, 5-13 an 17-33		is/are rejected.				
Claim(s)	is/are objected to.					
□ Claim(s)	·	are subject to restriction	or election			
Application Papers	is $\square$ approved t	requirement				
☐ The proposed drawing correction, filed on is/are objected	•	□ disapproved.				
☐ The specification is objected to by the Examiner.	a to by the Examine					
☐ The oath or declaration is objected to by the Examiner.						
Priority under 35 U.S.C. § 119 (a)–(d)	da=051150 5440 (a)	(-I)				
<ul> <li>□ Acknowledgement is made of a claim for foreign priority un</li> <li>□ All □ Some* □ None of the:</li> </ul>	der 35 U.S.C. § 119 (a)	–(a).				
☐ Certified copies of the priority documents have been red	eived					
☐ Certified copies of the priority documents have been red		0				
☐ Copies of the certified copies of the priority documents	• •					
in this national stage application from the International I		(a))				
*Certified copies not received:			•			
Attachment(s)						
☐ Information Disclosure Statement(s), PTO-1449, Paper No(s	s) 🗆 In	terview Summary, PTO-413				
☐ Notice of Reference(s) Cited, PTO-892	□ N	☐ Notice of Informal Patent Application, PTO-152				
☐ Notice of Draftsperson's Patent Drawing Review, PTO-948	□ <b>o</b>	ther				
Office Action Summary						

U.S. Patent and Trademark Office PTO-326 (Rev. 11/00)

Part of Paper No.

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The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims, 5-13 and 17-33 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matanaka in view of Gore.

Matanaka (figs. 2, 9, 26 and 27) teaches a multi layer ceramic package with a piezo element mounted in a cavity and protected by a cover on one side of substrate while an IC chip is formed on the opposite side of the substrate. Lead electrodes are taken out of the package on the same side as the IC chip is mounted. The IC chip and other components are encapsulated. A structure which includes the output electrodes projects beyond the IC and components, providing the necessary stand-off so that this side can be mounted to a printed circuit board. Solder balls are not explicitly used to connect the structure to the PCB. However, Gore (fig. 8), explicitly teaches using solder balls to both stand-off and electrically couple an electronic package to a printed circuit board. This allows connections to be made either by reflow soldering or ultrasonic bonding to form strong, mechanical and electrical and electrical connections. Thus at least those reasons it would have been obvious to one of ordinary skill in the art to use solder balls as e.g. #11a, #11b in Hatanaka.

Further cited of interest are Fukiharu and Yatsuda.

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